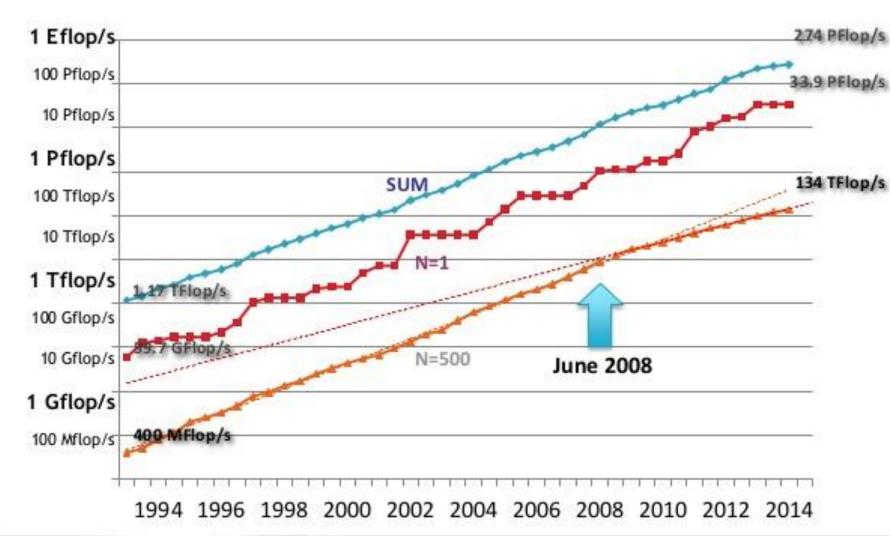
ParaFEM: A massively parallel alternative to Abaqus/Ansys for implicit solid mechanics

Dr Lee Margetts School of Mechanical, Aerospace and Civil Engineering http://parafem.org.uk

Advances in Hardware

Performance Development





Titan, Oak Ridge National Laboratory 20+ Petaflops

299,008 cores (Opteron) and 18,600 NVIDIA GPUs >20,000,000,000,000 floating point operations per second

1,000,000,000,000,000,000

AN EXASCALE COMPUTER WILL PERFORM ONE QUINTILLION OPERATIONS PER SECOND.

An exascale computer can perform as many calculations per second as about 50 MILLION LAPTOPS.



Current projections for power consumption of exascale computers is put at 100 MEGAWATTS – the same amount of power as ONE MILLION 100-WATT lightbulbs.

AN EXASCALE COMPUTER WILL BE **1,000 TIMES** FASTER

than today's most powerful supercomputer. FUJITSU'S K COMPUTER.

Today's fastest supercomputers are GIGANTIC requiring space the size of a football field.

2018?

Scientists hope to build an exascale computer by 2018 with the Europe, China, Japan and the U.S. all investing hundreds of millions of \$\$\$.

The processing power will transform sciences such as astrophysics and biology as well as improving climate modelling and national security.



http://larreks.deviantart.com/art/Evolution-of-Tomb-Raider-425582963

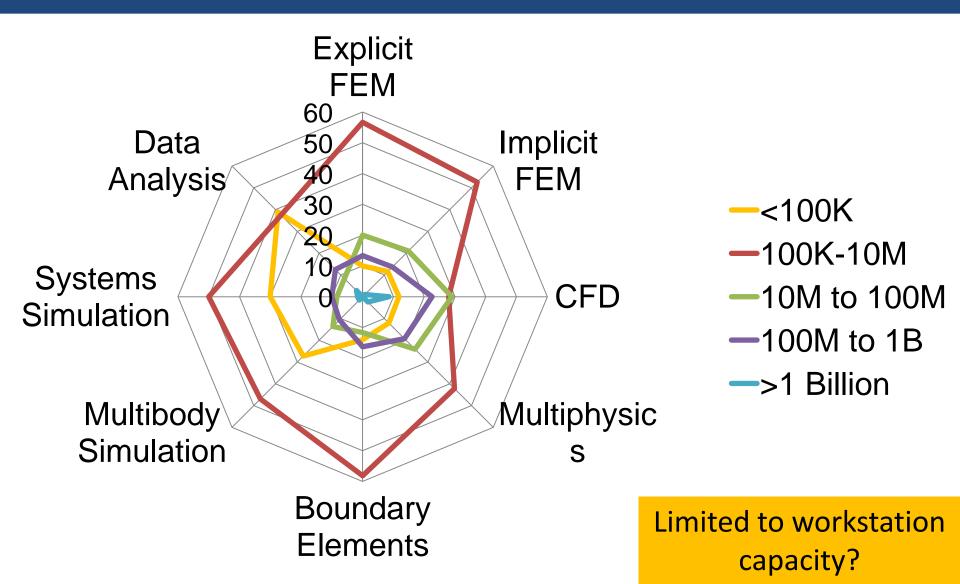
#1 in 1996?



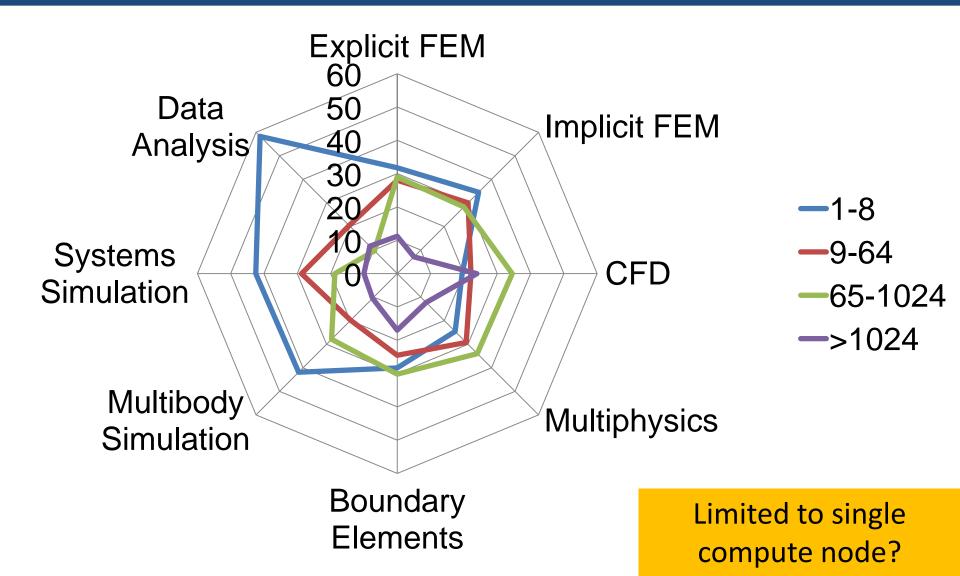
A8 Processor SoC ~172GFlops?

Engineering Simulation

NAFEMS Survey 2014 – Problem Size



NAFEMS Survey 2014 – Number of Cores



Large 3D finite element problems

Mesh Subdivision	Number of Equations	
10 x 10 x 10	12,580	
20 x 20 x 20	98,360	
40 x 40 x 40	777,520	
80 x 80 x 80	6,182,240	
100 x 100 x 100	12,059,800	
400 x 400 x 400	768,959,200	
440 x 440 x 440	1,023,368,720	

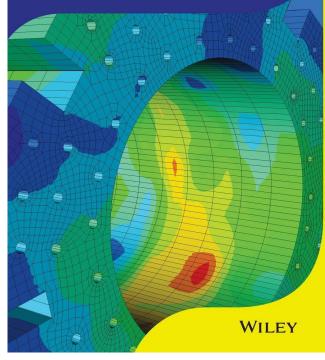


Tomography 4000 x 4000 x 4000 voxels = 10^{11} to 10^{12} dof

ParaFEM – General Purpose Parallel FE

Fifth Edition Programming the Finite Element Method

I. M. Smith, D. V. Griffiths and L. Margetts



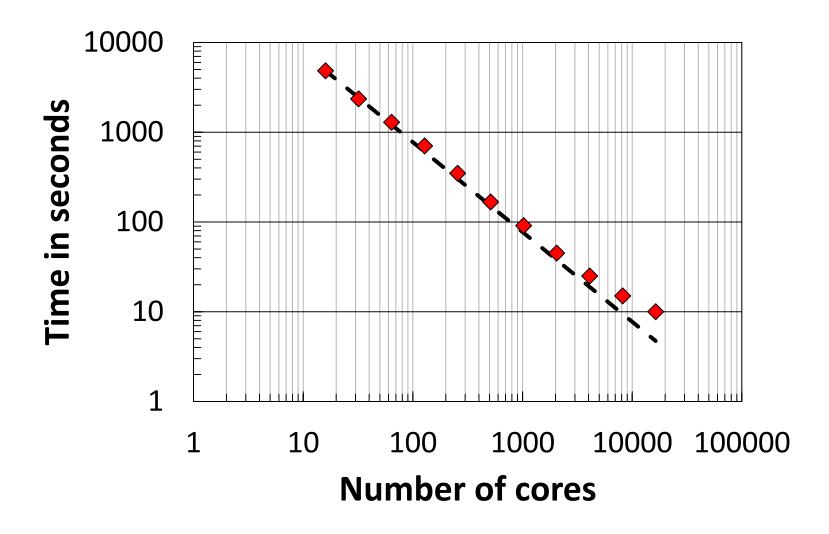
– Fortran + MPI

- Open source engineering package
- ~64,000 cores
- >1 billion degrees of freedom
- Similar functionality to Ansys, Abaqus
- Used for teaching and research
- 750+ registered on website
- ~1000 citations of text book
- http://parafem.org.uk
- http://www.amazon.com/Programmin g-Finite-Element-Method-Smith/dp/1119973341

Time for one solution step - elasto-plasticity

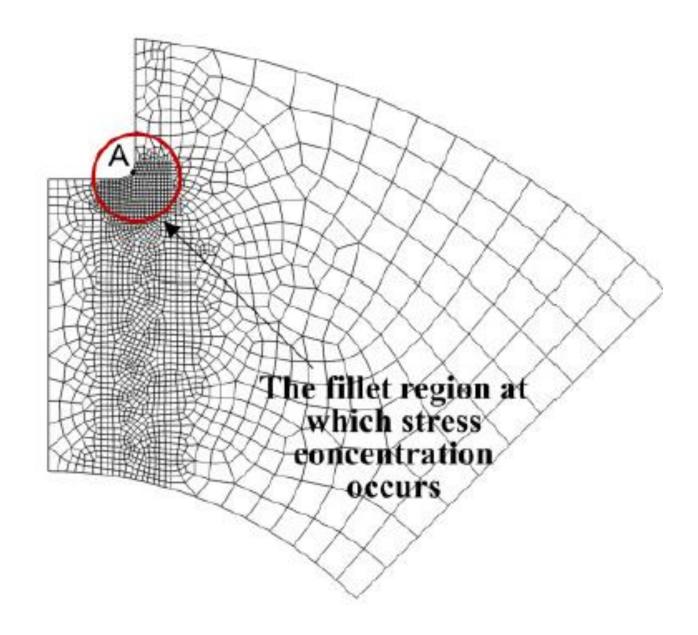
Mesh (equations)	Processes	Time (secs)
12,059,800	16	486
	32	256
	64	140
	128	83
768,959,200	1024	2721
	2048	1213
	4096	662

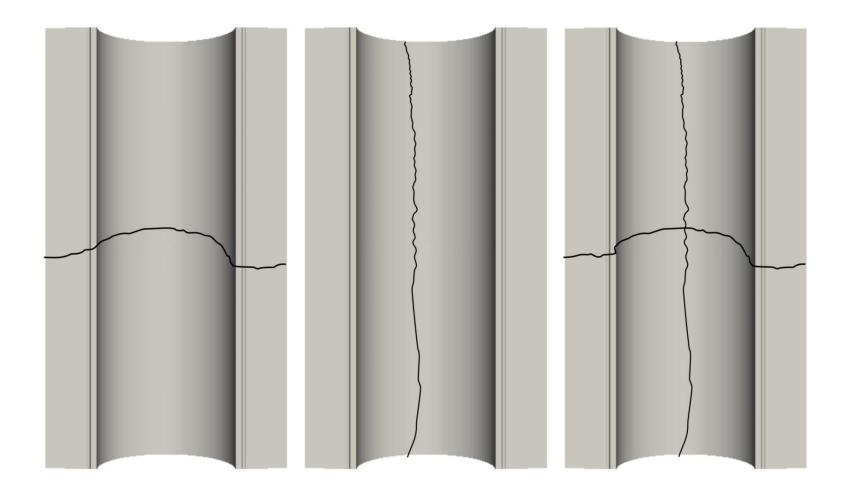
Time for one solution step – transient thermal

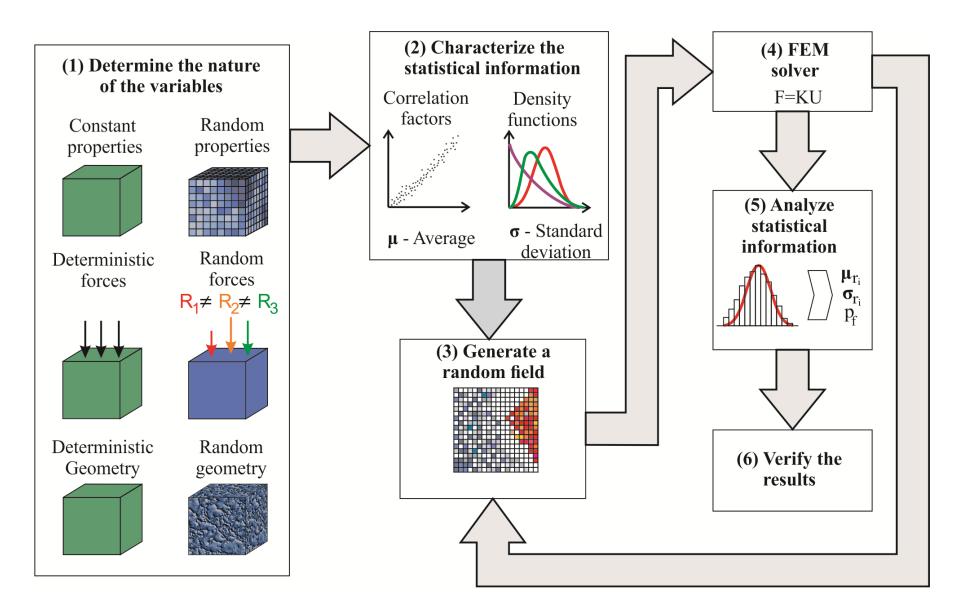


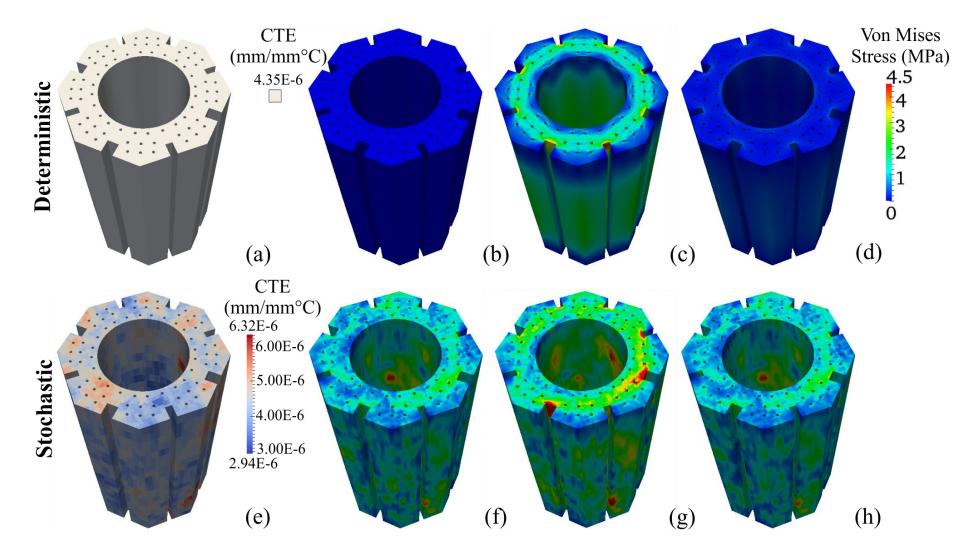
Monte Carlo Simulation

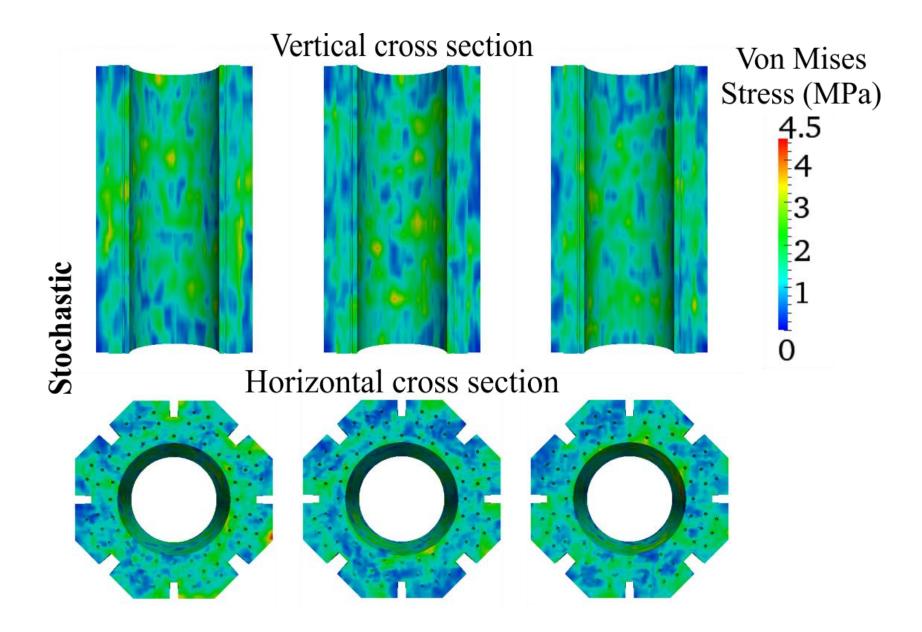










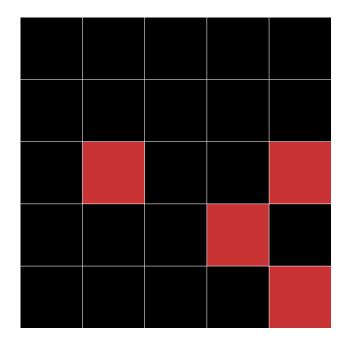


Multi-scale Simulation

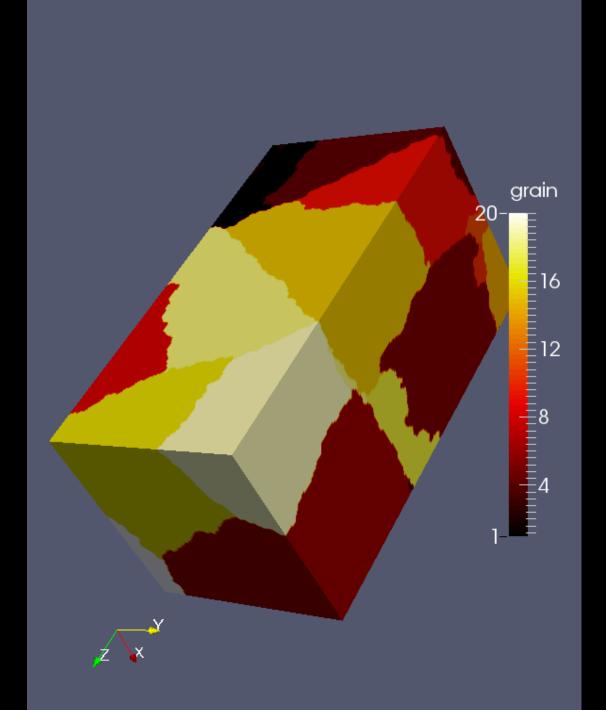
Meso-scale models

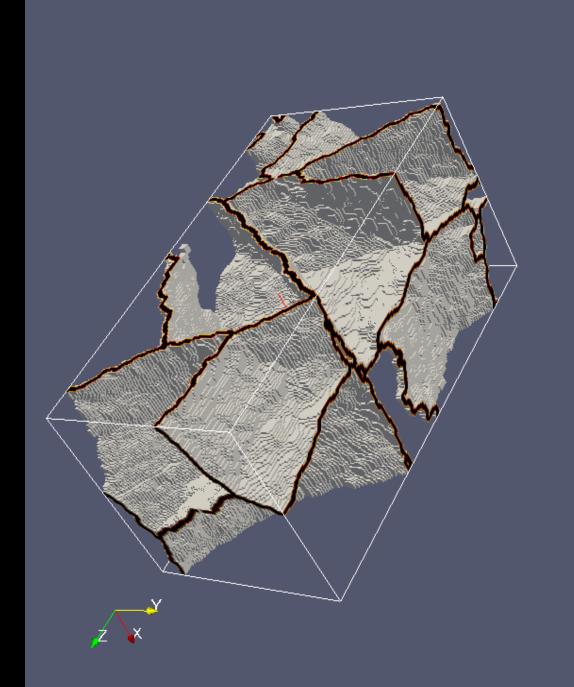
- Lattice-based or cellular automata ...
- Simulate mechanisms at grain scale
- Emergent behaviour such as fatigue/fracture
- Iterative 2-level process
 - Meso-scale updates FE scale continuum properties
 - FE computes new stresses

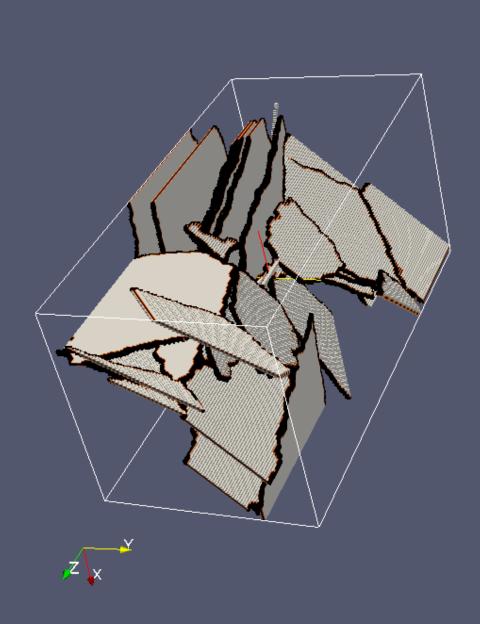
Cellular automata



Shterenlikht A. and Howard I.C. (2006) "The CAFE model of fracture – application to a TMCR steel", Fatigue and Fracture of Engineering Materials and Structures, Volume 29, Issue 9-10







Multiphysics

EPSRC CASE PhD Studentship

Couple OpenFOAM + ParaFEM Fluid-structure interaction in wind farms

ALS



The University of Manchester Aerospace Research Institute PhD Student in cohesive fracture Modelling damage in CFC Abaqus UEL interface in ParaFEM



Other Activities



THE UNIVERSITY of EDINBURGH



Nonlinear geometry with plasticity Abaqus UMAT interface

User Upgrade Program Available TODAY Intel[®] Xeon Phi[™] Product Family Industry and User Momentum Announcing 1 TFLOPS¹ 3+ TFLOPS² 2H'15 Knights Bootable Processor Knights Knights -On-Pkg, High BW Memory First -Integrated Fabric Landing Systems Corner Hill **3**rd Generation Intel[®] Xeon Phi[™] inte **Product Family** Knights Landing 2nd Generation **Intel Omni-Path** O systems providers expected³ Architecture many more Intel® Xeon Phi™ card-based systems 10nm process Coprocessor - Applications technology and Solutions Catalog >100 PFLOPS customer system compute commits to-date³

¹Claim based on calculated theoretical peak double precision performance capability for a single coprocessor. 16 DP FLOPS/clock/core * 61 cores * 1.23GHz = 1.208 TeraFLOPS ²Over 3 Teraflops of peak theoretical double-precision performance is preliminary and based on current expectations of cores, clock frequency and floating point operations per cycle. FLOPS = cores x clock frequency x floating-point operations per second per cycle. ³ Intel internal estimate

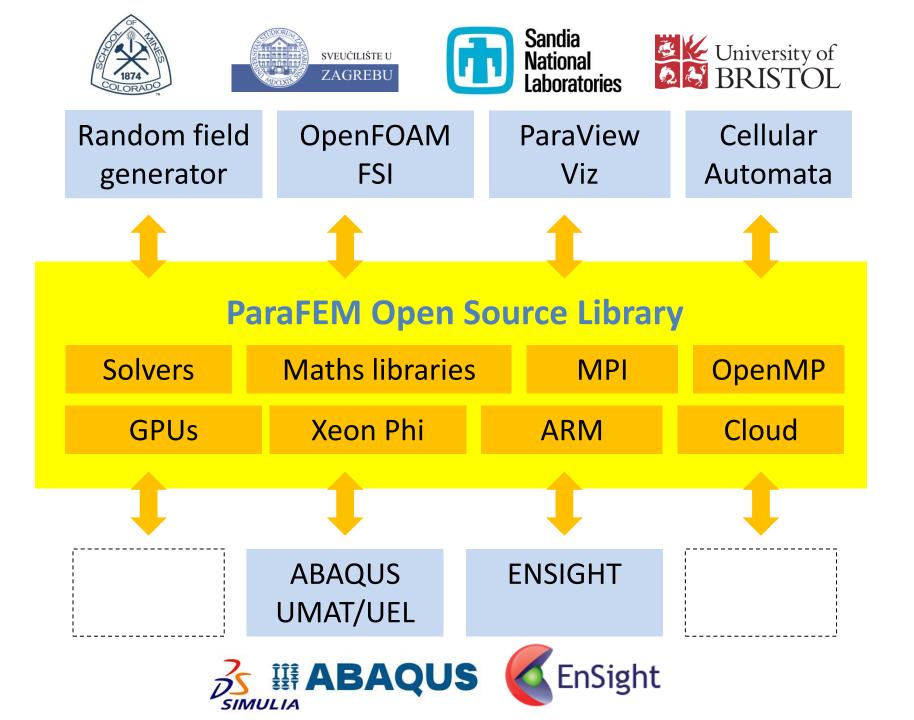


3,120,000 cores (Intel Ivy Bridge and Xeon Phi) 33,860,000,000,000,000 floating point operations per second

Summary

ParaFEM Open Source Library

Solvers	Maths libraries	MPI	OpenMP
GPUs	Xeon Phi	ARM	Cloud



Summary

- Aiming to build the next generation platform for engineering simulation ~2018-2020
- Software engineering focus on "plug and play" interfaces between OSS/proprietary code
- No need to reinvent the wheel
- Development work is research-driven
- Strong interest from industry, USA and China